

WEST**Generate Collection****Search Results - Record(s) 1 through 3 of 3 returned.**☐ 1. Document ID: US 6179489 B1

L2: Entry 1 of 3

File: USPT

Jan 30, 2001

US-PAT-NO: 6179489

DOCUMENT-IDENTIFIER: US 6179489 B1

TITLE: Devices, methods, systems and software products for coordination of computer main microprocessor and second microprocessor coupled thereto

DATE-ISSUED: January 30, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
So; John Ling Wing	Plano	TX	N/A	N/A
Kerr; Jeffrey L.	Garland	TX	N/A	N/A
Magee; Steven R.	Carrollton	TX	N/A	N/A
Tang; Jun	Richardson	TX	N/A	N/A

US-CL-CURRENT: 709/102

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 2. Document ID: US 6105119 A

L2: Entry 2 of 3

File: USPT

Aug 15, 2000

US-PAT-NO: 6105119

DOCUMENT-IDENTIFIER: US 6105119 A

TITLE: Data transfer circuitry, DSP wrapper circuitry and improved processor devices, methods and systems

DATE-ISSUED: August 15, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kerr; Jeffrey L.	Garland	TX	N/A	N/A
So; John Ling Wing	Plano	TX	N/A	N/A
Magee; Steven R.	Carrollton	TX	N/A	N/A

US-CL-CURRENT: 711/219; 710/129

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 3. Document ID: US 5909559 A

L2: Entry 3 of 3

File: USPT

Jun 1, 1999

US-PAT-NO: 5909559

DOCUMENT-IDENTIFIER: US 5909559 A

TITLE: Bus bridge device including data bus of first width for a first processor, memory controller, arbiter circuit and second processor having a different second data width

DATE-ISSUED: June 1, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
So; John Ling Wing	Plano	TX	N/A	N/A

US-CL-CURRENT: 710/127; 710/129, 710/131

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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